



Dudeck 3-6-11-34-7

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicants(s): Dudeck et al.  
Case: 3-6-11-34-7  
Serial No.: 10/764,000  
Filing Date: January 23, 2004  
Examiner: Unassigned  
Group: Unassigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: *Gene Maurice* Date: March 8, 2004

Title: Method and Apparatus for Reducing Leakage Current in a Read Only Memory Device Using Transistor Bias

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TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attention: Official Draftsperson

Sir:

Applicants submit herewith three (3) sheets of formal drawings in the above-referenced application.

Respectfully submitted,

*Kevin M. Mason*

Date: March 8, 2004

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